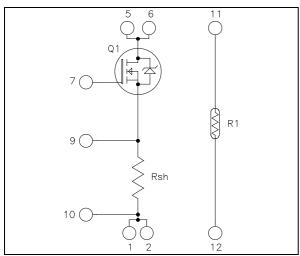
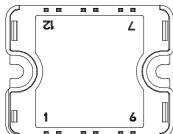
Linear MOSFET Power Module





Pins 1/2; 5/6 must be shorted together

$$\begin{split} V_{DSS} &= 100V \\ R_{DSon} &= 09 m\Omega \text{ typ @ Tj} = 25^{\circ}C \\ I_D &= 154A^{*} \text{ @ Tc} = 25^{\circ}C \end{split}$$

Application

• Electronic load dedicated to power supplies and battery discharge testing

Features

- Linear MOSFET
- Very low stray inductance
- Internal thermistor for temperature monitoring
- High level of integration
- AlN substrate for improved thermal performance

Benefits

- Direct mounting to heatsink (isolated package)
- easy series and parallels combinations for power and voltage improvements
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		100	V
Ţ	Continuous Drain Current		154*	
I_D	Continuous Drain Current	$T_c = 80$ °C	115*	A
I_{DM}	Pulsed Drain current	430		
V_{GS}	Gate - Source Voltage		±30	V
R _{DSon}	Drain - Source ON Resistance		10	mΩ
P_{D}	Maximum Power Dissipation •	480	W	
I_{AR}	Avalanche current (repetitive and non repetitive)		100	A
E_{AR}	Repetitive Avalanche Energy		50	mJ
E_{AS}	Single Pulse Avalanche Energy		3000	1113

- * Output current must be limited to 67A @ T_C =25°C and 47A @ T_C =80°C to not exceed the shunt specification.
- In saturation mode

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

All ratings @ $T_j = 25$ °C unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 100V$	$T_j = 25^{\circ}C$			100		
		$V_{GS} = 0V, V_{DS} = 80V$	$T_j = 125$ °C			500	μA	
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 69.5A$			9	10	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 2.5 \text{mA}$		2		4	V	
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$				±100	nA	

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		9875		
C_{oss}	Output Capacitance	$V_{\rm DS} = 25V$		3940		pF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		1470		

Shunt Electrical Characteristics

Symbol	Characteristic		Min	Typ	Max	Unit
R_{sh}	Resistance value			4.4		mΩ
T_{sh}	Tolerance			2		%
P_{sh}	L L Oad Capacity	T _C =25°C			20	W
		T _C =80°C			10	VV
$I_{\rm sh}$	L urrent canacity	T _C =25°C			67	٨
		T _C =80°C			47	Α

Temperature sensor PTC

1 chipci	ature sensor r re					
Symbol	Characteristic		Min	Typ	Max	Unit
R ₂₅	Resistance @ 25°C		1980		2020	Ω
R_{100}/R_{25}	Resistance ratio	Tamb=100°C & 25°C	1.676	1.696	1.716	
R_{-55}/R_{25}	Resistance ratio	Tamb=-55°C & 25°C	0.48	0.49	0.50	
В	Temperature coefficient			7900		ppm/K

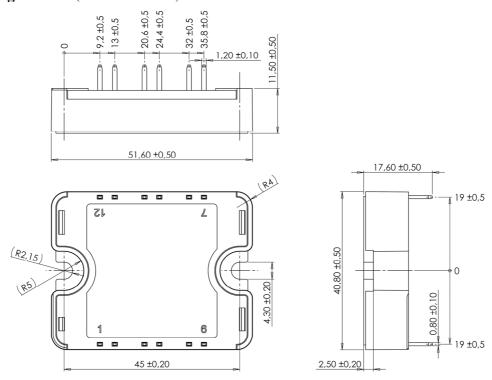
Thermal and package characteristics

Symbol	Characteristic		Min	Typ	Max	Unit	
R_{thJC}	Junction to Case Thermal Resistance		MOSFET			0.26	°C/W
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T_{J}	Operating junction temperature range			-40		150	
T_{STG}	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					80	g

2 - 4

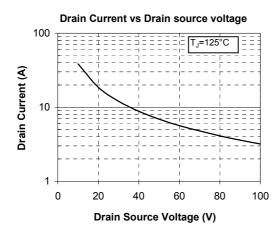


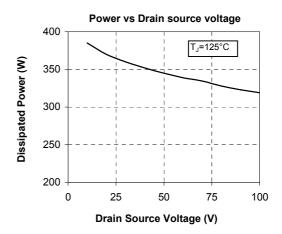
SP1 Package outline (dimensions in mm)



See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

Typical Performance Curve (linear mode)







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